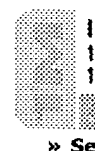


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L6	3	(current adj (conveyor or convertor or converter)) and ((low adj voltage) adj (current adj mirror))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/15 15:42
L7	2	(current adj (conveyor or convertor or converter)) and ((low adj voltage) adj (current adj mirror)) and MOSFET	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/15 15:43

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 Communications, Circuits and Systems, 2004. ICCAS 2004. 2004 International Conference on , Volume: 2 , 27-29 June 2004
 Pages:1156 - 1160 Vol.2

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2 High performance CMOS realization of the third generation current conveyor (CCIII)
Minaei, S.; Yildiz, M.; Kuntman, H.; Turkoz, S.;
 Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Symposium on , Volume: 1 , 4-7 Aug. 2002
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3 A high performance class AB current conveyor
Kurashina, T.; Ogawa, S.; Watanabe, K.;
 Electronics, Circuits and Systems, 1998 IEEE International Conference on , Volume: 3 , 7-10 Sept. 1998
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4 IEE Colloquium 'Analogue Signal Processing' (Digest No.1994/185)

Analogue Signal Processing, IEE Colloquium on , 13 Oct 1994

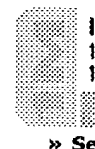
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Rajput, S.S.; Jamuar, S.S.;

Circuits and Systems, 2002. APCCAS '02. 2002 Asia-Pacific Conference on , Volume: 1 , 28-31 Oct. 2002

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Kiat-Seng Yeo; Zhi-Hui Kong; Wang-Ling Goh; Qingxiang Zhang; Wee-Guan Y
 VLSI Technology, Systems, and Applications, 2003 International Symposium on , 2003

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3 Auto-zero stabilized CMOS amplifiers for very low voltage or current offset
Dzahini, D.; Ghazlane, H.;

Nuclear Science Symposium Conference Record, 2003 IEEE , Volume: 1 , 19-2 Oct. 2003

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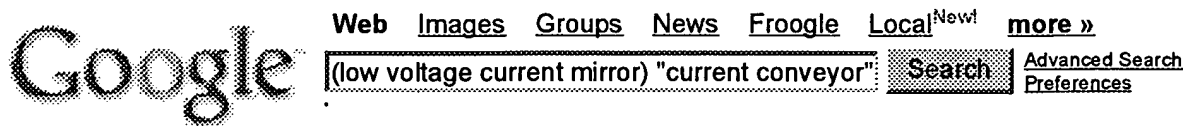
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